

DIODES ARE PURCH 101
TRANSISTORS ARE PURCH 100



AVIN	C.A.C.	ISSUE No	1	2	3
DESIGNED	CS 456	AR No	1374	1505	1997
DRAWN	W.P.	DATE	26-4-66	29-6-66	26-4-67
CHECKED	W.P.	INITIALS	C.A.C.	R.V.C.	S.S.S.

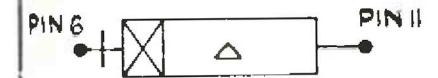
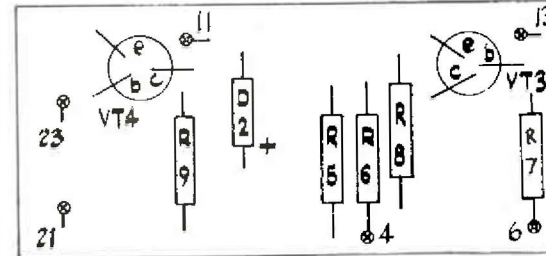
ELLIOTT BROTHERS (LONDON) LTD.

L.S.A. 18
SINGLE I/P NOISE REJECTION INVERTER. 920B

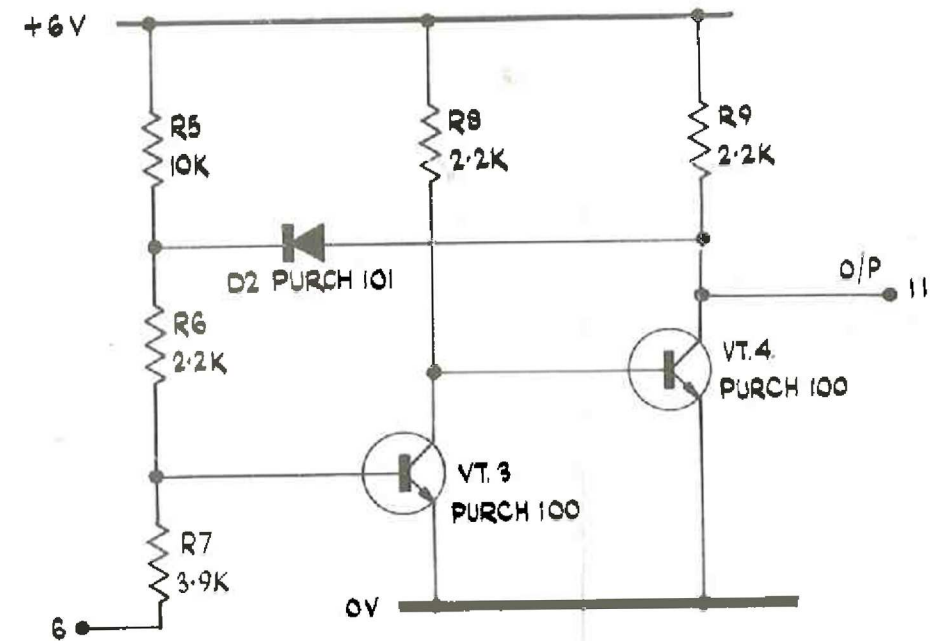
INSTRUCTION SHEET

322A7191

SHEET No 15 OF



ONLY USED FOLLOWING LSA 20 OR 21. COMBINATION CIRCUIT GIVES DELAY (Δ) ± 10ms/μF WHERE μF IS CAPACITANCE OF LSA 20 OR LSA 21.



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AVIN	C.A.C.	ISSUE No	1	2	3
DESIGNED	CS 456	AR No	1374	1505	1997
DRAWN	W.P.	DATE	26-4-66	8-7-66	26-4-67
CHECKED	W.P.	INITIALS	C.A.C.	R.V.C.	S.S.S.

ELLIOTT BROTHERS (LONDON) LTD.

L.S.A. 19
DELAY 920B

INSTRUCTION SHEET

322A7191

SHEET No 16 OF